

EMRA51D2J-27.095M

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REGULATORY COMPLIANCE (Data Sheet downloaded on Dec 14, 2019)



◀ Click badges to download compliance docs

Regulatory Compliance standards are subject to updates by governing bodies. Click the badges to download the latest compliance docs for this part number directly from Ecliptek.



ITEM DESCRIPTION

MEMS Clock Oscillators LVCMOS (CMOS) 1.8Vdc 4 Pad 1.6mm x 2.0mm Plastic Surface Mount (SMD) 27.095MHz \pm 20ppm over 0°C to +70°C

ELECTRICAL SPECIFICATIONS

| | |
|--|---|
| Nominal Frequency | 27.095MHz |
| Frequency Tolerance/Stability | \pm 20ppm Maximum over 0°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, and Output Load Change) |
| Aging at 25°C | \pm 1.5ppm Maximum First Year |
| Supply Voltage | 1.8Vdc \pm 10% |
| Input Current | 5mA Maximum (No Load) |
| Output Voltage Logic High (Voh) | 90% of Vdd Minimum (IOH = -2mA) |
| Output Voltage Logic Low (Vol) | 10% of Vdd Maximum (IOL = +2mA) |
| Rise/Fall Time | 1.5nSec Typical, 3.5nSec Maximum (Measured from 20% to 80% of waveform) |
| Duty Cycle | 50 \pm 5(%) (Measured at 50% of waveform) |
| Load Drive Capability | 15pF Maximum |
| Output Logic Type | CMOS |
| Output Control Function | Power Down (Disabled Output: Logic Low) |
| Output Control Input Voltage Logic High (Vih) | 70% of Vdd Minimum or No Connect to Enable Output |
| Output Control Input Voltage Logic Low (Vil) | 30% of Vdd Maximum to Disable Output |
| Power Down Output Enable Time | 5mSec Maximum |
| Power Down Output Disable Time | 150nSec Maximum |
| Standby Current | 5 μ A Maximum |
| Period Jitter (RMS) | 2pSec Typical, 5pSec Maximum |
| RMS Phase Jitter (Fj = 900kHz to 7.5MHz; Random) | 0.5pSec Typical, 1pSec Maximum |
| RMS Phase Jitter (Fj = 12kHz to 20MHz; Random) | 1.5pSec Typical, 3pSec Maximum |
| Start Up Time | 5mSec Maximum |
| Storage Temperature Range | -65°C to +150°C |

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

| | |
|----------------------|--|
| ESD Susceptibility | JESD22-A114, HBM, 2000V |
| Flammability | UL94-V0 |
| Mechanical Shock | MIL-STD-883, Method 2002, Condition E, 10,000G |
| Moisture Sensitivity | J-STD-020, MSL 1 |
| Solderability | MIL-STD-883, Method 2003 (Four I/O Pads on bottom of package only) |
| Temperature Cycling | JESD22-A104, Condition B |
| Vibration | MIL-STD-883, Method 2007, Condition A, 20G |

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MECHANICAL DIMENSIONS (all dimensions in millimeters)



| PIN | CONNECTION |
|-----|------------------------|
| 1 | Power Down (Logic Low) |
| 2 | Ground |
| 3 | Output |
| 4 | Supply Voltage |

| LINE | MARKING |
|------|---|
| 1 | Ecliptek Manufacturing Identifier |
| 2 | Ecliptek Manufacturing Identifier (continued) |

Suggested Solder Pad Layout

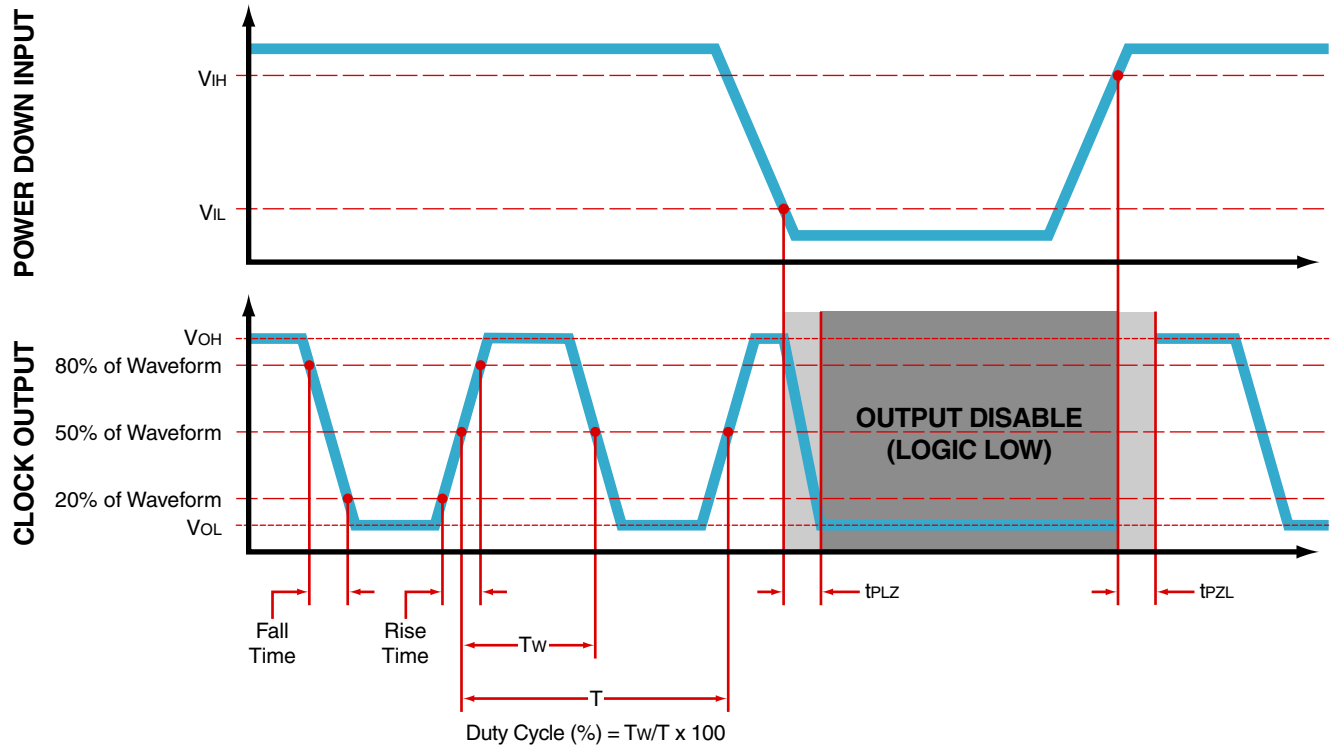
All Dimensions in Millimeters



All Tolerances are ± 0.1

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OUTPUT WAVEFORM & TIMING DIAGRAM



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Test Circuit for CMOS Output



Note 1: An external $0.01\mu\text{F}$ ceramic bypass capacitor in parallel with a $0.1\mu\text{F}$ high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low input capacitance ($<12\text{pF}$), 10X Attenuation Factor, High Impedance ($>10\text{Mohms}$), and High bandwidth ($>300\text{MHz}$) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance. See applicable specification sheet for 'Load Drive Capability'.

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Recommended Solder Reflow Methods



High Temperature Infrared/Convection

| | |
|---|--|
| $T_s \text{ MAX}$ to T_l (Ramp-up Rate) | $3^\circ\text{C/Second Maximum}$ |
| Preheat | |
| - Temperature Minimum ($T_s \text{ MIN}$) | 150°C |
| - Temperature Typical ($T_s \text{ TYP}$) | 175°C |
| - Temperature Maximum ($T_s \text{ MAX}$) | 200°C |
| - Time ($t_s \text{ MIN}$) | 60 - 180 Seconds |
| Ramp-up Rate (T_l to T_p) | $3^\circ\text{C/Second Maximum}$ |
| Time Maintained Above: | |
| - Temperature (T_l) | 217°C |
| - Time (t_l) | 60 - 150 Seconds |
| Peak Temperature (T_p) | $260^\circ\text{C Maximum for 10 Seconds Maximum}$ |
| Target Peak Temperature ($T_p \text{ Target}$) | $250^\circ\text{C} +0/-5^\circ\text{C}$ |
| Time within 5°C of actual peak (t_p) | 20 - 40 Seconds |
| Ramp-down Rate | $6^\circ\text{C/Second Maximum}$ |
| Time 25°C to Peak Temperature (t) | 8 Minutes Maximum |
| Moisture Sensitivity Level | Level 1 |
| Additional Notes | Temperature shown are applied to body of device. |

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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

| | |
|--|--|
| Ts MAX to Tl (Ramp-up Rate) | 5°C/Second Maximum |
| Preheat | |
| - Temperature Minimum (Ts MIN) | N/A |
| - Temperature Typical (Ts TYP) | 150°C |
| - Temperature Maximum (Ts MAX) | N/A |
| - Time (ts MIN) | 60 - 120 Seconds |
| Ramp-up Rate (Tl to Tp) | 5°C/Second Maximum |
| Time Maintained Above: | |
| - Temperature (Tl) | 150°C |
| - Time (tL) | 200 Seconds Maximum |
| Peak Temperature (Tp) | 240°C Maximum |
| Target Peak Temperature (Tp Target) | 240°C Maximum 2 Times / 230°C Maximum 1 Time |
| Time within 5°C of actual peak (tp) | 10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time |
| Ramp-down Rate | 5°C/Second Maximum |
| Time 25°C to Peak Temperature (t) | N/A |
| Moisture Sensitivity Level | Level 1 |
| Additional Notes | Temperature shown are applied to body of device. |

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperature shown are applied to body of device.)

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperature shown are applied to body of device.)