

# Frequently Asked Questions

Rev B

For Series:

EQRM12

## 1. What is this oscillator series?

This series of devices are oscillators where the output frequency is primarily controlled by an internal quartz bulk acoustic wave (BAW) crystal resonator and an integrated low-voltage positive emitter-coupled logic (LVPECL) circuit. Utilizing a proprietary crystal resonator design and exclusive oscillator design techniques, this series of oscillators is calibrated to a specified frequency prior to shipment to the customer.

## 2. What are the typical customer circuit applications for this oscillator series?

This oscillator series can be used in any of the following applications:

- Clock recovery
- Phase-locked loops (PLL's)
- Frequency modulation/demodulation
- Reference signal tracking
- Synthesizers clock
- Clock synchronization
- Digital switching

## 3. What are the typical applications and end item products for this series?

Here is a list of the common applications and products:

- ADSL and HDSL customer premise equipment (CPE)
- Hybrid fiber coax equipment (HFC)
- Cable modems
- SONET/ATM/SDH equipment
- MPEG audio/video equipment
- 1G, 2G, 4G, 10G fiber channel
- Gigabit ethernet
- PCI express

**4. What technical benefits does this product series offer?**

This series of crystal oscillator offers:

- Improved frequency stability through the use of a bulk acoustic wave (BAW) quartz crystal resonator
- Use of a fundamental mode oscillator application specific integrated circuit (ASIC) design
- LVPECL output with controlled rise and fall times
- Complementary output
- Output enable function (high impedance output)
- Excellent rms phase jitter performance
- Superior phase noise performance
- Tight duty cycle option
- Commercial, extended commercial, and industrial operating temperature range options
- Industry standard moisture sensitivity level 1 (MSL1) rated six pad ceramic SMD package
- RoHS Compliant (Pb-free) with high temperature 260°C reflow capability

**5. What are the construction characteristics of this product series?**

This product series consists of a single ASIC and a fundamental mode BAW quartz crystal packaged inside a hermetically sealed ceramic leadless SMD package. The leadless SMD package has gold plated contact I/O pads. The package has a seam sealed metal cover that is case grounded for improved EMI performance.

**6. What is the input supply voltage for this product series?**

The nominal supply voltage and tolerance is provided on the datasheet.

**7. What are the input current specifications for this product series?**

The input current specification is listed in milliamps as a maximum value and is provided on the datasheet.

**8. How do I specify the overall frequency stability for this product series?**

Ecliptek defines the frequency stability performance of the device inclusive of specific oscillator operating conditions. Ecliptek specifies the following inclusive parameters for this series of product:

- Calibration frequency tolerance at 25°C
- Frequency stability over operating temperature range
- Supply voltage
- Output load

The frequency stability specification can be found on the datasheet.

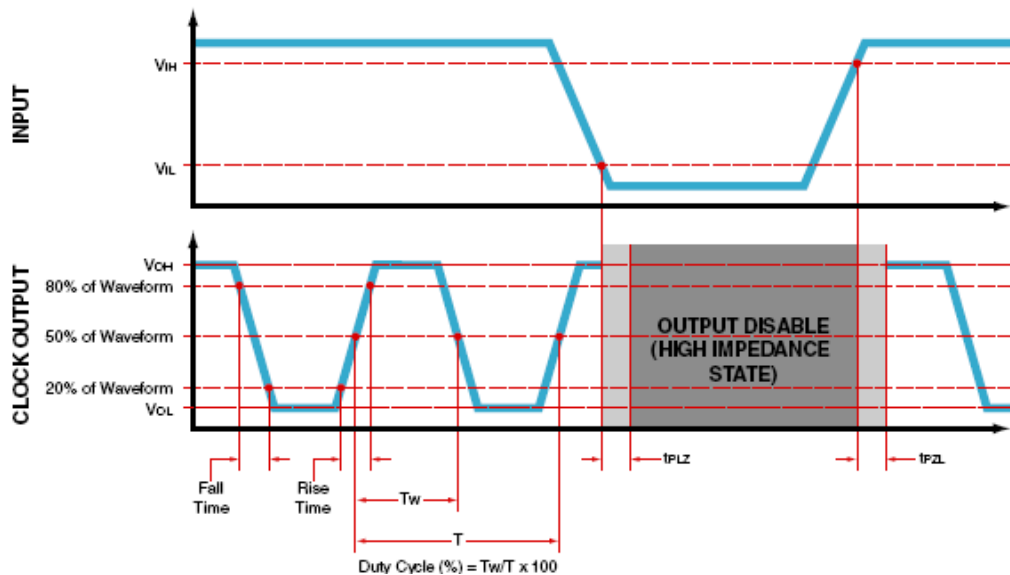
**9. What are the operating temperature range options for this product series?**

The operating temperature range is defined as the minimum and maximum temperatures that the oscillator can be exposed to during oscillation. Over this temperature range, all of the specified operating parameters are guaranteed. Ecliptek offers commercial, extended commercial and industrial temperature ranges. The available operating temperature range options can be found on the datasheet.

**10. Does this series offer an output enable function?**

This product series offers an output enable function to facilitate the customer's use of in-process assembly testing or for the use of multiple clocks on the same node. It features an output control function where the output is disabled (high impedance) when the voltage at the control pad is set to a logic low state. In this condition (see the figure below), the oscillator output buffer circuitry within the oscillator continues to operate and the output pad is placed into a high impedance state. If the voltage at the control pad is set to no connect or a logic high state, the output is enabled (clocking).

Note: The oscillator has an internal pull up resistor on the control pad. The datasheet provides the  $V_{IH}$  and  $V_{IL}$  thresholds for control of the output enable function.



**Figure: Output Enable Timing Diagram**

The time from when the oscillator pad input control reaches  $V_{IL}$  and the oscillator output pad becomes high impedance is the output disable time (shown as  $t_{PLZ}$ ). The time from when the oscillator pad input control reaches  $V_{OH}$  and the oscillator output begins clocking is the output enable time (shown as  $t_{PZL}$ ). The output disable time and enable time specifications can be found on the datasheet.

**11. What is oscillator aging and what are the aging specifications for this product series?**

Aging is the systematic change in frequency with time due to internal changes in the crystal and/or oscillator. Aging is often expressed as a maximum value in parts per million per year [ppm/year]. The rate of aging is logarithmic in nature. The following factors effect oscillator aging: adsorption and desorption of contamination on the surfaces of the quartz, stress relief of the mounting and bonding structures, material outgassing, and seal integrity. The oscillator aging specification can be found on the datasheet.

**12. What are the period jitter, phase noise, and phase jitter specifications for this product series?**

Period jitter is a time domain measurement and is specified in picoseconds (pS) as a maximum value. Phase noise is a measure in the frequency domain and is specified in decibels at various offset points from the carrier (-dBc/Hz). Phase jitter, often called offset jitter, is derived from the phase noise measurement of the spectral density over a given offset bandwidth. Ecliptek uses a proprietary design, exclusive processing methods, and a unique ASIC output driver circuit enabling this product series to have exceptionally low period jitter, phase jitter, and phase noise. The period jitter, rms phase jitter, and phase noise specifications can be found on the datasheet.

**13. Is tight duty cycle (symmetry) available for this product series?**

Duty cycle is the measure of output waveform uniformity. This term, also referred to as symmetry, is a measurement of the time that the output waveform is in a logic high state, expressed as a percentage (%) of the clock period. This parameter is measured at a specified voltage threshold or at a percentage of the output waveform amplitude. The duty cycle specification is listed on the datasheet.

**14. What are the output and output load characteristics for this product series?**

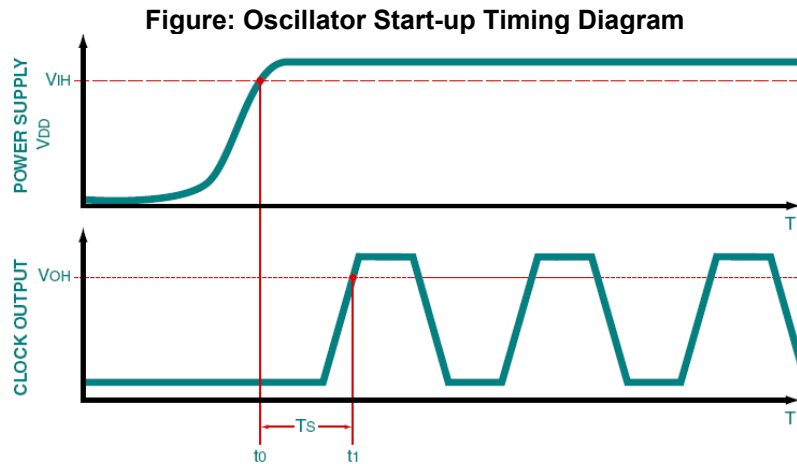
Ecliptek offers this product series with a low-voltage positive emitter-coupled logic (LVPECL) output. The oscillator output topology utilizes an open collector transistor that allows the end user to configure their output termination so as to optimize circuit load matching and signal performance. Signal integrity is optimized when the low impedance output of the oscillator is driving a fifty ohm transmission line. Thus, Ecliptek specifies a load termination of fifty ohms into  $V_{DD}$  minus  $2.0V_{DC}$ . This load configuration assures that any signal being reflected from the load will be absorbed by the source. The output load specification is listed on the datasheet.

**15. What is the function of the complementary output on pad five of the oscillator?**

The term complementary output, often called a differential pair, is when one output signal is the logical opposite (complement) of the other output signal. Thus, when the output (pad four) of the oscillator is in a logic high state, the complementary output (pad five) of the oscillator is in a logic low state.

**16. Is start-up time specified for this product series?**

As shown in the figure below, start-up ( $T_s$ ) time is defined as the time ( $t_0$ ) from when the power supply reaches its specified  $V_{IH}$  value to the time ( $t_1$ ) the oscillator output signal amplitude reaches its steady state  $V_{OH}$  output logic high level and the output is within the specified frequency tolerance.



The maximum start-up time specification can be found on the datasheet. Note: The complementary output waveform is not shown in the above figure.

**17. How do I electrically test this product series at my facility?**

The recommended electrical test fixture can be found on the datasheet.

**18. Is this product series compatible with my existing assembly process equipment?**

If the part number is specified with the TR packaging option, oscillator products are delivered to the customer in EIA481 compliant tape and reel packaging. Without the TR option, products are delivered to the customer in bulk packaging as specified on the datasheet.

**19. Is this product series compatible with my existing reflow processes?**

This product series is capable of withstanding industry standard high temperature (260°C, 10 seconds) convection reflow processes and is rated MSL1 per J-STD-020. The suggested solder reflow diagram is provided on the datasheet.

**20. Is this product series RoHS compliant and Pb-free?**

This product is RoHS compliant and Pb-free as defined in the [Ecliptek RoHS Compliant \(Pb-free\) Roadmap](#).

**21. How can I obtain a RoHS compliant (Pb-free) certification for this product series?**

A RoHS and Pb-free product certification letter can be obtained directly from our website by using the [Ecliptek RoHS/Pb-Free Certification Letter Generator](#).

- 22. Is an IPC-1752 material declaration available for customer review?**  
Ecliptek can provide a [Material Declaration](#) in compliance with IPC-1752 to assist customers with their material compliance requirements.
- 23. How do I layout my printed circuit board (PCB) for this product series?**  
A suggested solder pad layout is provided on the datasheet. The customer should layout their PCB to include proper connections for the output enable function.
- 24. Who do I contact if I have additional technical questions about the use of this product series?**  
The [Global Customer Support](#) team at Ecliptek can provide applications engineering support or answer customer technical questions.
- 25. How do I order an oscillator that has custom requirements not specified on the standard oscillator series specification sheet?**  
Please contact the Ecliptek [Global Customer Support](#) team for additional support or questions regarding your oscillator requirements.
- 26. What are the environmental and mechanical specifications for this product series?**  
The environmental and mechanical specifications for this product series are listed on the datasheet.
- 27. What reliability information is available for this product series?**  
Failure in Time (FIT) and Mean Time to Failure (MTTF) reliability data is available for this product series within the product series qualification and reliability report found on the series home page.
- 28. Is thermal resistance information available for this product series?**  
 $\theta_{JA}$  and  $\theta_{JC}$  values are available for this product series and can be found on the Environmental / Mechanical section of the series homepage.
- 29. Is IBIS model information available for this product series?**  
Input/Output Buffer Information Specification (IBIS) modeling information can be found on the series home page.
- 30. What is the marking scheme for this product series?**  
As shown on the datasheet, this series of product has marking content on the top of the part. This marking consists of a pad one locator dot and additional lines of alpha numeric marking. The datasheet provides the marking content.
- 31. Can I identify the Ecliptek part number or specification based upon the markings on top of the part?**  
In order to protect our customer's intellectual property, the Ecliptek part marking does not identify the Ecliptek part number or specifications.

**32. Where can I get the information regarding discontinued or End of Life (EOL) products?**

Any Ecliptek part number currently under an End of Life statement will be identified as EOL on Ecliptek's quotation, along with a link to the EOL statement. This information can also be found on the [Product End of Life \(EOL\) Announcements](#) section of our website.

**33. Is Ecliptek ISO 9000 Certified?**

Yes, Ecliptek is [ISO 9001:2008](#) certified.

**34. How can I obtain a REACH compliance statement for this product series?**

A Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH) compliance statement can be obtained directly from our website by using the [Ecliptek REACH Compliance Resources](#) page of our website.

**35. How can I obtain a Conflict Minerals Report for this product series?**

A Conflict Minerals Reporting Template can be obtained directly from our website by using the [Ecliptek Conflict Minerals Compliance Resources](#) page of our website.