

### **Ecliptek's New LVPECL Oscillator Series Provides Frequency Range to 625MHz, Reduces Lead Times**

*Available in 2.5VDC or 3.3VDC, new EMRC MEMS SMD oscillators offer improved jitter performance*

**May 16, 2012**

Costa Mesa, CA - Ecliptek Corporation is pleased to announce the introduction of four new low-voltage positive emitter-coupled logic (LVPECL) surface mount MEMS based oscillator series. These new [EMRC series](#) further enhance Ecliptek's extensive offering of quartz and MEMS based frequency control products, delivering high frequency operation, complementary outputs, and exceptional jitter performance.

"These four new series of LVPECL output oscillators enable our customers to obtain clocking solutions that meet their stringent period jitter and rms phase jitter performance requirements," said Thomas Culhane, Vice President of Engineering of Ecliptek Corporation. "The use of a differential output clock with low jitter is a key performance requirement for customer storage, network, and telecom applications."

The new 2.5V<sub>DC</sub> EMRC12 (5mm x 7mm) and EMRC22 (3.2mm x 5mm) series as well as the 3.3V<sub>DC</sub> EMRC13 (5mm x 7mm) and EMRC23 (3.2mm x 5mm) series are available with output frequencies up to 625.000MHz in industry standard six pad SMD packaging. Manufactured with frequency stabilities as low as  $\pm 20$ ppm, these RoHS compliant MEMS oscillators are available in commercial, extended commercial, and industrial temperature range options.

*Founded in 1987, Ecliptek has become a leading supplier of frequency control products to the electronics industry. Ecliptek provides complete engineering support, unparalleled customer service and innovative products to their OEM customers and distributors worldwide. Complete information on company operations or any of Ecliptek's quality frequency control devices can be obtained by visiting Ecliptek's internet site at [www.ecliptek.com](http://www.ecliptek.com). The company's email address is [customersupport@ecliptek.com](mailto:customersupport@ecliptek.com)*