

EQUD32E7H5-18.432M

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REGULATORY COMPLIANCE (Data Sheet downloaded on Jul 11, 2020)


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ITEM DESCRIPTION

Temperature Compensated Voltage Controlled Quartz Crystal Clock Oscillators TCVCXO Clipped Sinewave 2.5Vdc 4 Pad 2.5mm x 3.2mm Ceramic Surface Mount (SMD) 18.432MHz -30°C to +85°C

ELECTRICAL SPECIFICATIONS

Nominal Frequency	18.432MHz
Frequency Stability	±1.5ppm Maximum (Inclusive of Operating Temperature Range, at Vdd=2.5Vdc and Vc=1.4Vdc)
Frequency Stability vs. Frequency Tolerance	±1.0ppm Maximum (at 25°C ±2°C, at Vdd=2.5Vdc, and Vc=1.4Vdc, Pre-Reflow)
Frequency Stability vs. Input Voltage	±0.2ppm Maximum (±5%)
Frequency Stability vs. Load	±0.2ppm Maximum (±1kOhm//±1pF)
Frequency Stability vs. Reflow	±1ppm Maximum (at 25°C, 24 hours after reflow, 1 time)
Frequency Stability vs. Aging	±1ppm/Year Maximum (at 25°C)
Operating Temperature Range	-30°C to +85°C
Supply Voltage	2.5Vdc ±5%
Input Current	2.0mA Maximum
Output Voltage	0.8Vp-p Clipped Sinewave Minimum (External DC-Cut capacitor required, 1000pF recommended)
Load Drive Capability	10kOhms//10pF
Output Logic Type	Clipped Sinewave
Control Voltage	1.4Vdc ±1.0Vdc
Frequency Deviation	±5ppm Minimum
Linearity	10% Maximum
Transfer Function	Positive Transfer Characteristic
Modulation Bandwidth	10kHz Minimum at -3dB
Input Impedance	500kohm Minimum
Phase Noise	All Values are Typical -89dBc/Hz at 10Hz Offset -115dBc/Hz at 100Hz Offset -138dBc/Hz at 1kHz Offset -150dBc/Hz at 10kHz Offset
RMS Phase Jitter	0.7pSec Typical, 1pSec Maximum (Fj = 12kHz to 20MHz (Random))
Start Up Time	2mSec Maximum
Storage Temperature Range	-40°C to +85°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003

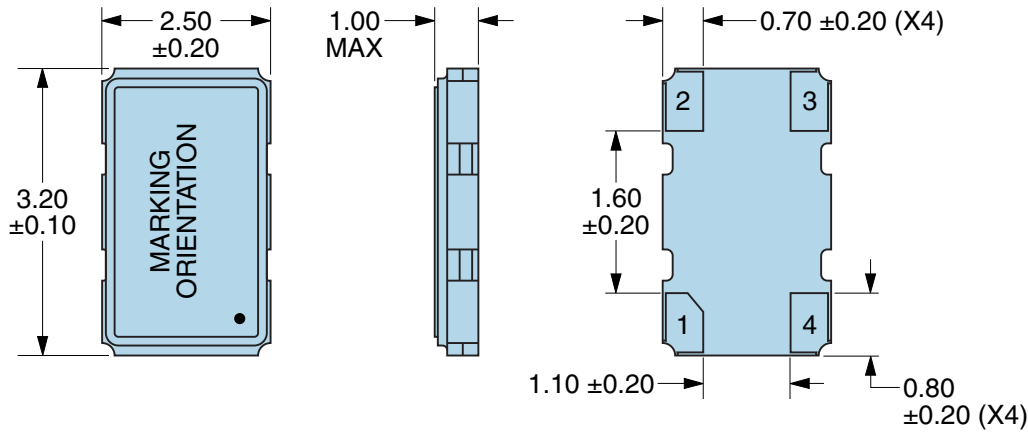
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ENVIRONMENTAL & MECHANICAL SPECIFICATIONS CONTINUED

Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A

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MECHANICAL DIMENSIONS (all dimensions in millimeters)

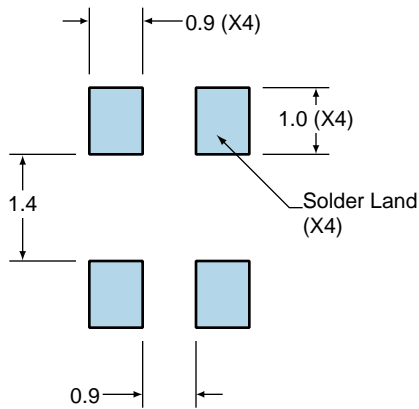


PIN	CONNECTION
1	Control Voltage
2	Case/Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	E18.432 E=Ecliptek Designator
2	XXXXX XXXXX=Ecliptek Manufacturing Identifier

Suggested Solder Pad Layout

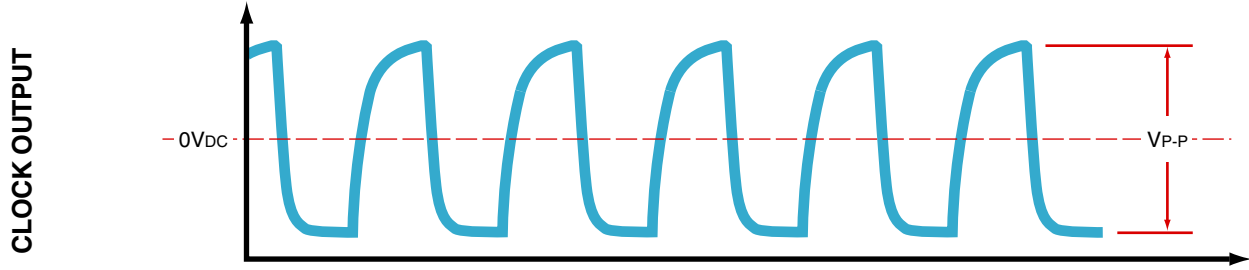
All Dimensions in Millimeters



All Tolerances are ± 0.1

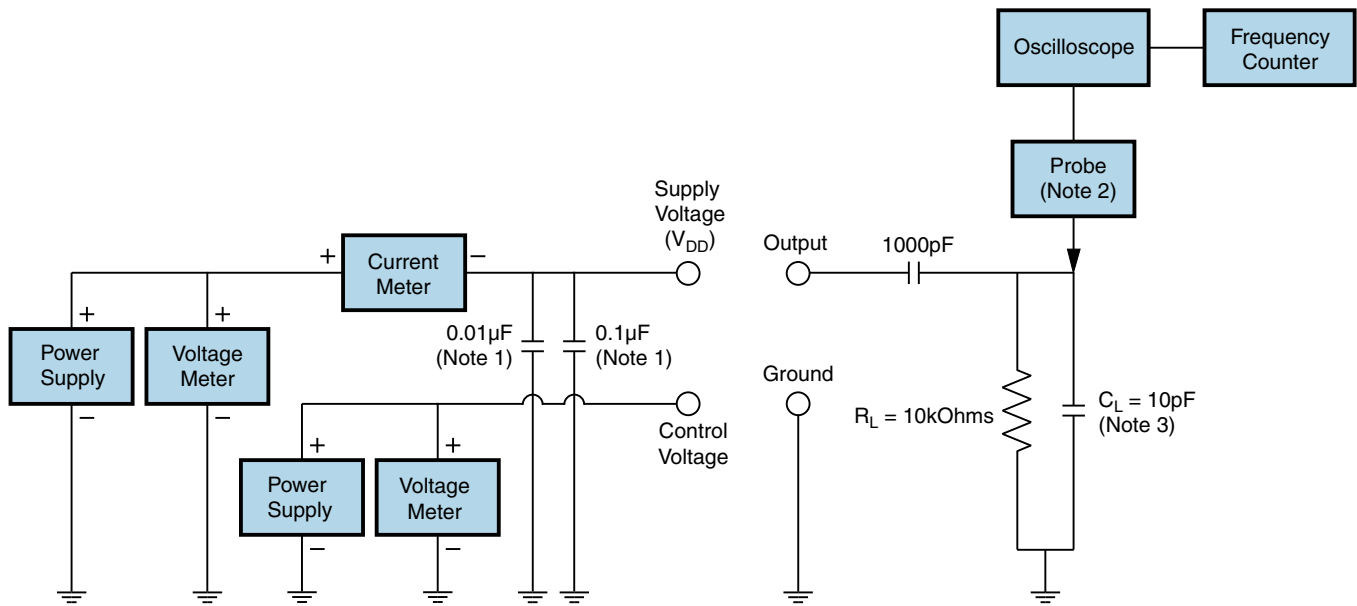
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OUTPUT WAVEFORM



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Test Circuit for Clipped Sinewave Output



Note 1: An external 0.01 μF bypass capacitor in parallel with a 0.1 μF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low input capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.